

SFP-WDM-SM-0180B

155Mbps SFP Bi-Directional Transceiver, 80km Reach 1550nm TX / 1490nm RX

Features

- Up to 155Mbps data-rate
- 1550nm DFB laser and PIN photodetector for 80km transmission
- Compliant with SFP MSA and SFF-8472 with simplex LC or SC receptacle
- Digital Diagnostic Monitoring:
 Internal Calibration or External Calibration
- · Compatible with RoHS
- +3.3V single power supply
- Operating case temperature range of 0°C to +70°C (Commercial) or -40°C to +85°C (Industrial)



Applications

- SDH STM-1, S-1.1,L-1.1, L-1.2
- SONET OC-3 IR1, LR1, LR2
- · Other optical links

Description

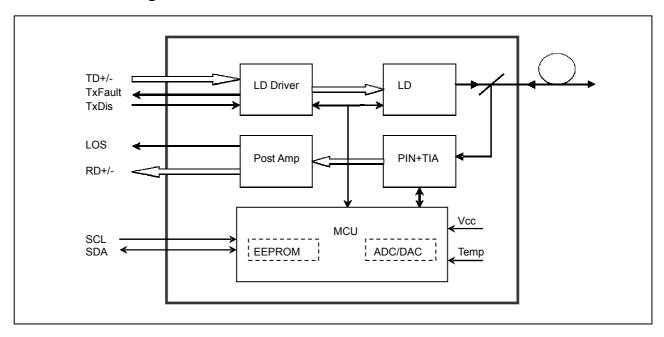
The SFP-BIDI transceivers are high performance, cost effective modules supporting data-rate of 155Mbps and 80km transmission distance with SMF.

The transceiver consists of three sections: a DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement (MSA) and SFF-8472. For further information, please refer to SFP MSA.



Module Block Diagram



Absolute Maximum Ratings

	<u> </u>			
Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Parameter		Symbol	Min	Typical	Max	Unit
Operating Cose Temperature	Commercial		0		+70	°C
Operating Case Temperature	Industrial	Tc	-40		+85	°C
Power Supply Voltage		Vcc	3.13	3.3	3.47	V
Power Supply Current		Icc			300	mA
Data Rate				155		Mbps



Optical and Electrical Characteristics

Para	ımeter	Symbol	Min	Typical	Max	Unit	Notes
		Transmitt	er				
Centre Wavelength		λς	1530	1550	1570	nm	
Spectral Width (-20dB)		Δλ			1	nm	
Side Mode Suppression	Ratio	SMSR	30				
Average Output Power		Pout	-5		0	dBm	1
Extinction Ratio		ER	10		18	dB	
Optical Rise/Fall Time (2	0%~80%)	t _r /t _f			0.3	ns	
Data Input Swing Differe	ntial	Vin	400		1800	mV	2
Input Differential Impeda	nce	Z _{IN}	90	100	110	Ω	
TV 5: 11	Disable		2.0		Vcc	V	
TX Disable	Enable		0		0.8	V	
	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
		Receive	r			_	'
Centre Wavelength		λς	1470		1510	nm	
Receiver Sensitivity					-34	dBm	3
Receiver Overload			-3			dBm	3
LOS De-Assert		LOS _D			-35	dBm	
LOS Assert		LOS _A	-45			dBm	
LOS Hysteresis			1		4	dB	
Data Output Swing Differ	ential	Vout	400		1800	mV	4
		High	2.0		Vcc	V	
LOS		Low			0.8	V	

Notes

- 1. The optical power is launched into SMF.
- 2. PECL input, internally AC-coupled and terminated.
- 3. Measured with a PRBS 2²³-1 test pattern @155Mbps, BER≤1×10⁻¹⁰.
- 4. Internally AC-coupled.



Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock			400	KHz
MOD_DEF (0:2)-High	V _H	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

Diagnostics Specification

Diagnostics opeometricin					
Parameter	Range	Unit	Accuracy	Calibration	
Tomporatura	0 to +70	°C	±3°C	Internal / External	
Temperature	-40 to +85		±3 C		
Voltage	3.0 to 3.6	V	±3%	Internal / External	
Bias Current	0 to 100	mA	±10%	Internal / External	
TX Power	-5 to 0	dBm	±3dB	Internal / External	
RX Power	-32 to -8	dBm	±3dB	Internal / External	

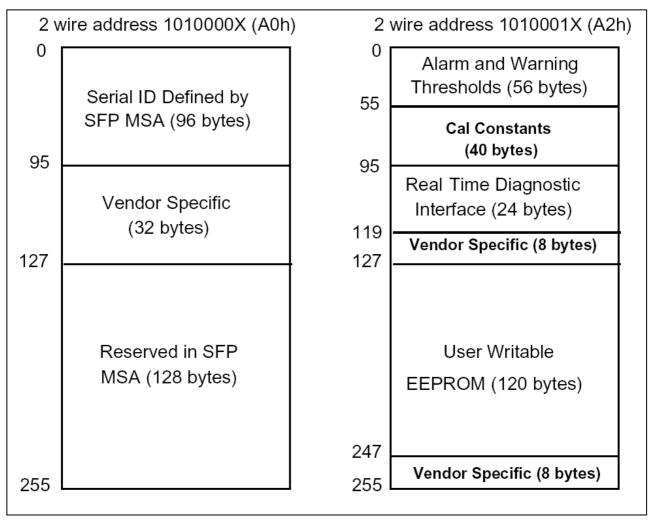


Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.





SFP Transceiver Electrical Pad Layout

	1			
20 VeeT	1 VeeT			
19 TD-	2 TxFault			
18 TD+	3 Tx Disable			
17 VeeT	4 MOD-DEF(2)			
16 VccT	5 MOD-DEF(1)			
15 VccR	6 MOD-DEF(0)			
14 VeeR	7 Rate Select			
13 RD+	8 LOS			
12 RD-	9 VeeR			
11 VeeR	10 VeeR			
Top of Board	Top of Board (as viewed thru top of board)			



Pin Descriptions

Pin	Signal Name	Description	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	
2	TX Fault	Transmitter Fault Indication	3	Note 1
3	TX Disable	Transmitter Disable	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3
5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	Rate Select	Not Connect	3	
8	LOS	Loss of Signal	3	Note 4
9	VeeR	Receiver ground	1	
10	VeeR	Receiver ground	1	
11	VeeR	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	VeeR	Receiver ground	1	
15	VccR	Receiver Power Supply	2	
16	VccT	Transmitter Power Supply	2	
17	VeeT	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6
19	TD-	Inv. Transmit Data In	3	Note 6
20	VeeT	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a $4.7k\sim10k\Omega$ resistor. Its states are:

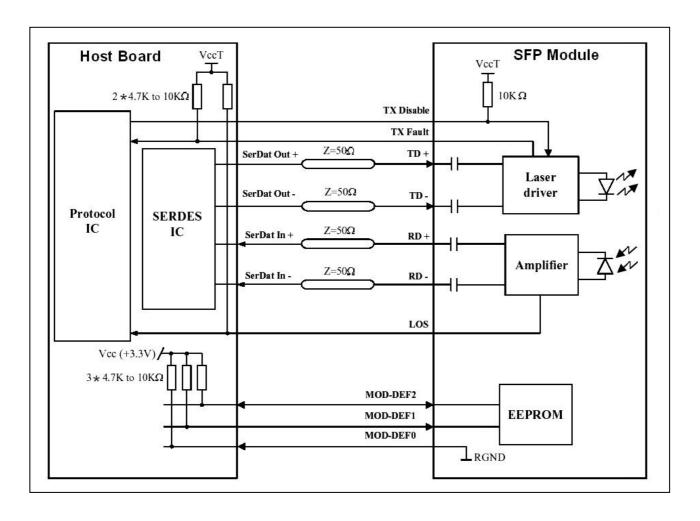
Low (0 to 0.8V): Transmitter on (>0.8V, < 2.0V): Undefined

High (2.0 to 3.465V): Transmitter Disabled Open: Transmitter Disabled

- 3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a $4.7k\sim10k\Omega$ resistor on the host board. The pull-up voltage shall be VccT or VccR
 - Mod-Def 0 is grounded by the module to indicate that the module is present
 - Mod-Def 1 is the clock line of two wire serial interface for serial ID
 - Mod-Def 2 is the data line of two wire serial interface for serial ID
- 4) LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor. Pull up voltage between 2.0V and Vcc+0.3V. Logic 1 indicates loss of signal; Logic 0 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
- 5) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 6) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

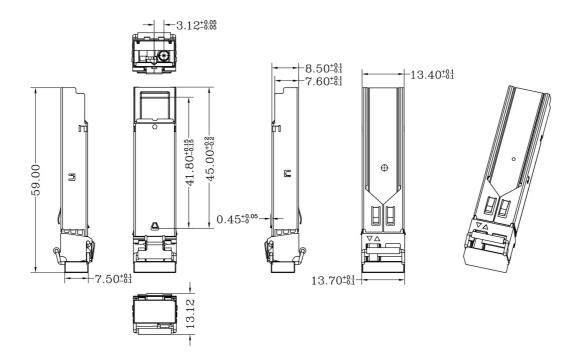


Recommended Interface Circuit





Mechanical Dimensions



Regulatory Compliance

Feature	Standard	Performance
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>500 V) Isolation with the case
Electromagnetic Interference (EMI)	FCC Part 15 Class B	Compatible with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN60950, EN (IEC) 60825-1,2	Compatible with Class I laser product. Compatible with TüV standards
Component Recognition	UL and CUL	UL file E317337
Green Products	2002/95/EC 2005/618/EC	RoHS6



Ordering information

Part Number	Product Description
SFP-WDM-SM-0180B	Tx1550/Rx1490nm, 155Mbps, LC, 80km, 0°C~+70°C
SFP-WDM-SM-0180DB	Tx1550/Rx1490nm, 155Mbps, LC, 80km, 0°C~+70°C, With Digital Diagnostic Monitoring
SFP-WDM-SM-0180IB	Tx1550/Rx1490nm, 155Mbps, LC, 80km, -40°C~+85°C
SFP-WDM-SM-0180DIB	Tx1550/Rx1490nm, 155Mbps, LC, 80km, -40°C~+85°C, With Digital Diagnostic Monitoring

References

- 1. Small Form Factor Pluggable (SFP) Transceiver Multi-Source Agreement (MSA), September 2000.
- 2. Telcordia GR-253-CORE and ITU-T G.957 Specifications.

Important Notice

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sales@optone.net
http://www.optone.net